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# Design Of Analog Fuzzy Logic Controllers In Cmos Technologies Implementation Test And Application 1

**chapter 12: printed circuit board (pcb) design issues** - basic linear design 12.4 figure 12.1: analog and digital circuits should be partitioned on pcb layout the layout of the evaluation board is optimized in terms of grounding, decoupling, and signal routing and can be used as a model when laying out the adc section of the pc **section 6 digital filters - analog** - digital filters 6.2 figure 6.1 however, it is possible to replace a general purpose dsp chip and design special hardware digital filters which will operate at video-speed sampling rates. **analog front-end design for ecg systems using delta-sigma adcs** - common-mode 50 hz to 60 hz 1.5 v 0 v electrode offset  $\pm 300$  mv ecg signal ecg signal  $\pm 2.5$  mv 0.05 hz to 150 hz  $\pm 2.5$  mv 0.05 hz to 150 hz nature of the ecg signal and system design implications ti recent technology advancements have resulted in adcs with a combination of speeds, resolution, and **psoc® 4 and psoc 6 mcu capsense® design guide** - psoc® 4 and psoc 6 mcu capsense® design guide, doc. no. 001-85951 rev. \*v 6 1. introduction 1.1 abstract the capsense® design guide shows how to design capacitive touch sensing applications with the capsense feature in psoc® 4 and psoc 6 mcu device families. the capsense feature in these devices offer unprecedented signal-to-noise ratio (snr), best-in-class liquid tolerance, and a wide ... **afe design for a narrowband power-line communications ...** - ti introduction 1 introduction the afe031 is an integrated power-line communications analog front-end device that consists of a variety of functional blocks; the device is driven by a microcontroller. **cmos analog switches - vishay intertechnology** - vishay siliconix dg300b, dg301b, dg302b, dg303b document number: 71402 s11-0303-rev. c, 28-feb-11 vishay 1 cmos analog switches features • analog signal range:  $\pm 15$  v **high-speed quad spst cmos analog switch** - vishay 2 document number: 70038 s-71241-rev. g, 25-jun-07 vishay siliconix dg201hs notes: a. signals on sx, dx, or inx exceeding v+ or v- will be clamped by internal diodes. limit forward diode current to maximum current ratings. **12-bit dac with eeprom memory in sot-23-6** - pin board. 2 sot-23 **table of contents - nasa infrared telescope facility** - resolvers are similar to synchros except they have two stator coils at right angles rather than three separated by 120°. the most simple resolver, the four-pole, is illustrated in figure 2-3. **microcontroller oscillator circuit design considerations** - order this document by an1706/d microcontroller oscillator circuit design considerations by cathy cox and clay merritt 1 introduction the heartbeat of every microcontroller design is the oscillator circuit.

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